

Lonten N-channel 70V, 65A, 8.2mΩ Power MOSFET

Description

These N-Channel enhancement mode power field effect transistors are using **shielded gate trench** DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Features

- ◆ 70V, 65A, $R_{DS(on).max} = 8.2\text{m}\Omega @ V_{GS} = 10\text{V}$
- ◆ Improved dv/dt capability
- ◆ Fast switching
- ◆ 100% EAS Guaranteed
- ◆ Green device available

Applications

- ◆ Motor Drives
- ◆ UPS
- ◆ DC-DC Converter

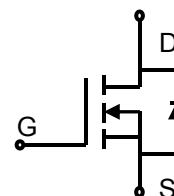
Product Summary

V_{DSS}	70V
$R_{DS(on).max} @ V_{GS}=10\text{V}$	8.2mΩ
I_D	65A

Pin Configuration



TO-252



N-Channel MOSFET

Absolute Maximum Ratings

$T_c = 25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	70	V
Continuous drain current ($T_c = 25^\circ\text{C}$)	I_D	65	A
($T_c = 100^\circ\text{C}$)		41	A
Pulsed drain current ¹⁾	I_{DM}	260	A
Gate-Source voltage	V_{GSS}	± 20	V
Avalanche energy ²⁾	E_{AS}	96	mJ
Power Dissipation	P_D	59.5	W
Storage Temperature Range	T_{STG}	-55 to +150	°C
Operating Junction Temperature Range	T_J	-55 to +150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.1	°C/W
Thermal Resistance, Junction-to-Ambient ³⁾	$R_{\theta JA}$	125	°C/W

Package Marking and Ordering Information

Device	Device Package	Marking	Units/Reel
LSGG07R082WE	TO-252	G07R082WE	2500

Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV_{DSS}	$V_{\text{GS}}=0 \text{ V}, I_D=250\mu\text{A}$	70	---	---	V
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	2.0	---	4.0	V
Drain-source leakage current	I_{DSS}	$V_{\text{DS}}=70 \text{ V}, V_{\text{GS}}=0 \text{ V}, T_J = 25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=70 \text{ V}, V_{\text{GS}}=0 \text{ V}, T_J = 150^\circ\text{C}$	---	---	10	mA
Gate leakage current, Forward	I_{GSSF}	$V_{\text{GS}}=20 \text{ V}, V_{\text{DS}}=0 \text{ V}$	---	---	100	nA
Gate leakage current, Reverse	I_{GSSR}	$V_{\text{GS}}=-20 \text{ V}, V_{\text{DS}}=0 \text{ V}$	---	---	-100	nA
Drain-source on-state resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10 \text{ V}, I_D=40 \text{ A}, T_J = 25^\circ\text{C}$	---	7.2	8.2	$\text{m}\Omega$
		$T_J = 150^\circ\text{C}$	---	12.2	---	
		$V_{\text{DS}}=5 \text{ V}, I_D=40 \text{ A}$	---	40	---	S
Dynamic characteristics						
Input capacitance	C_{iss}	$V_{\text{DS}} = 35 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 250 \text{ kHz}$	---	1424	---	pF
Output capacitance	C_{oss}		---	537	---	
Reverse transfer capacitance	C_{rss}		---	41	---	
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 35 \text{ V}, V_{\text{GS}} = 15 \text{ V}, I_D = 40 \text{ A}$	---	16	---	ns
Rise time	t_r		---	19	---	
Turn-off delay time	$t_{\text{d}(\text{off})}$		---	38.4	---	
Fall time	t_f		---	21.6	---	
Gate resistance	R_g	$V_{\text{GS}}=0 \text{ V}, V_{\text{DS}}=0 \text{ V}, f=1 \text{ MHz}$	---	2.0	---	Ω
Gate charge characteristics						
Gate to source charge	Q_{gs}	$V_{\text{DS}}=56 \text{ V}, I_D=40 \text{ A}, V_{\text{GS}}=10 \text{ V}$	---	7	---	nC
Gate to drain charge	Q_{gd}		---	10.9	---	
Gate charge total	Q_g		---	28.6	---	
Gate plateau voltage	V_{plateau}		---	5	---	
Output Charge	Q_{oss}	$V_{\text{DS}}=56 \text{ V}, V_{\text{GS}}=0 \text{ V}$	---	41	---	nC
Drain-Source diode characteristics and Maximum Ratings						
Continuous Source Current	I_s		---	---	49.5	A
Pulsed Source Current	I_{SM}		---	---	198	A
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0 \text{ V}, I_s=40 \text{ A}, T_J=25^\circ\text{C}$	---	---	1.2	V
Reverse Recovery Time	t_{rr}	$I_s=40 \text{ A}, dI/dt=100 \text{ A/us}, T_J=25^\circ\text{C}$	---	54	---	ns
Reverse Recovery Charge	Q_{rr}		---	49.7	---	nC

Notes:

1: Repetitive Rating: Pulse width limited by maximum junction temperature.

2: $V_{\text{DD}}=56 \text{ V}, V_{\text{GS}}=10 \text{ V}, L=0.5 \text{ mH}, I_{\text{AS}}=19.6 \text{ A}, R_G=25 \Omega$, Starting $T_J=25^\circ\text{C}$.3: The value of R_{thJA} is measured by placing the device in a still air box which is one cubic foot.

Electrical Characteristics Diagrams

Figure 1. Typ. Output Characteristics

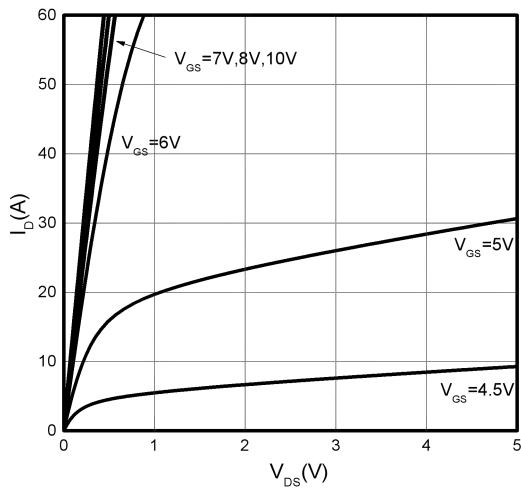


Figure 2. Transfer Characteristics

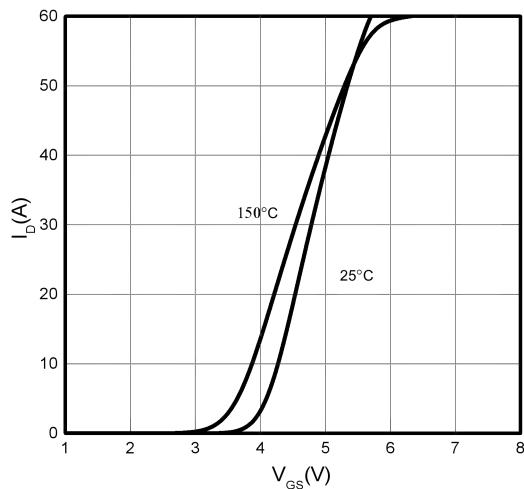


Figure 3. On-Resistance vs.Drain Current

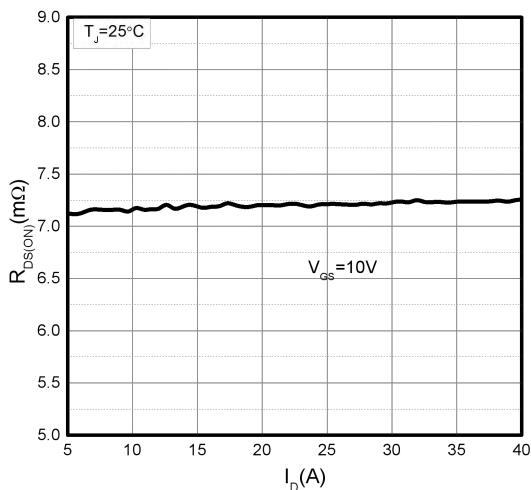


Figure 4.On-Resistance vs.Temperature

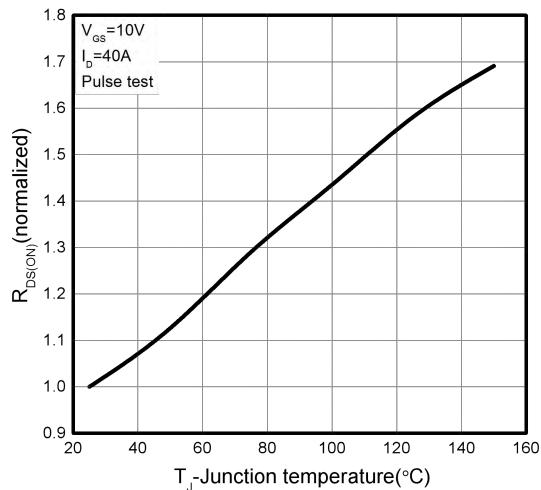


Figure 5.Breakdown Voltage vs.Temperature

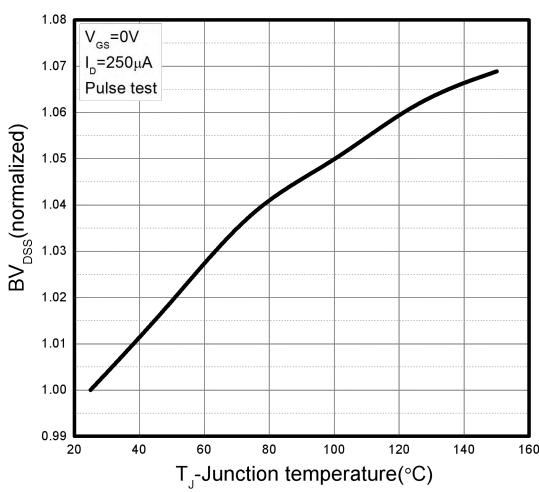


Figure 6.Threshold Voltage vs.Temperature

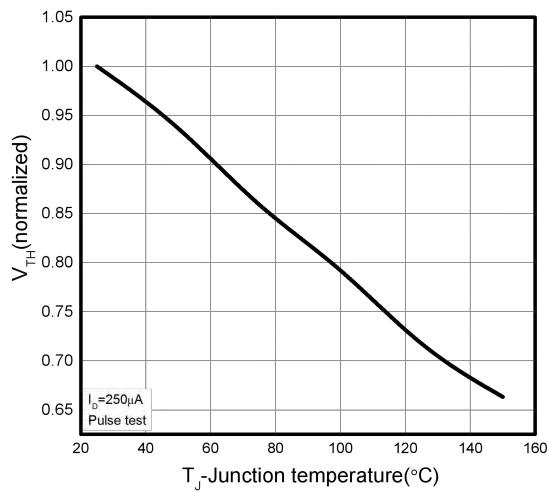


Figure 7.R_{ds(on)} vs. Gate Voltage

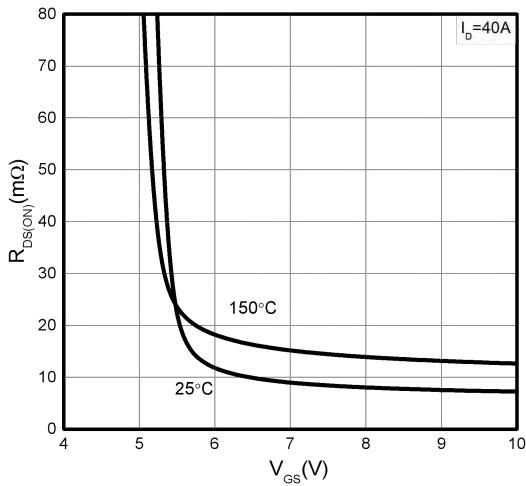


Figure 8.Body-Diode Characteristics

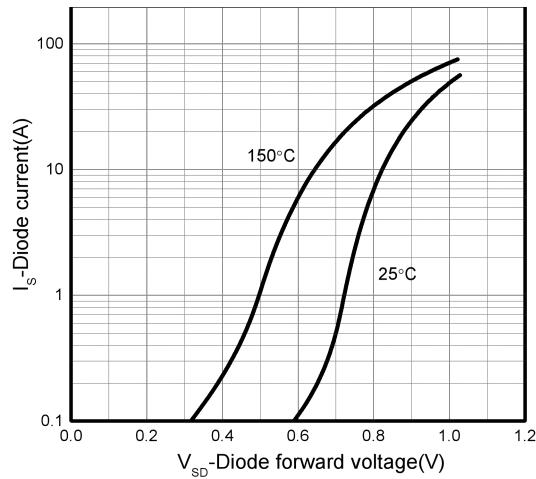


Figure 9.Capacitance Characteristics

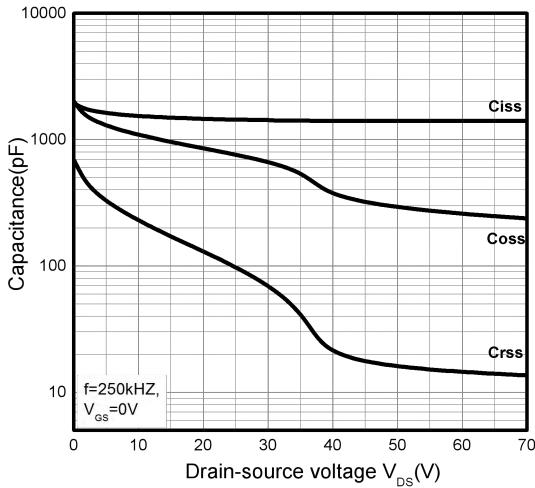


Figure 10.Gate Charge Characteristics

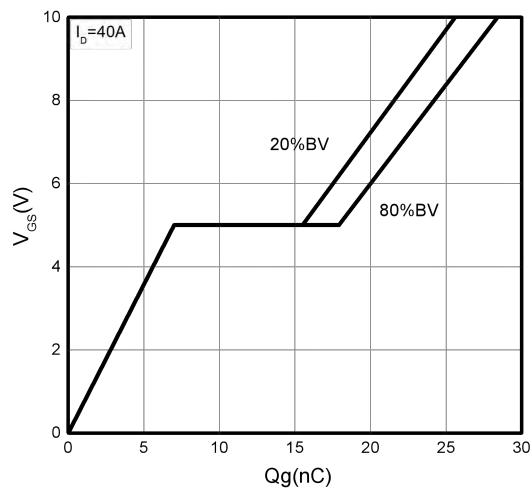


Figure 11.Drain Current Derating

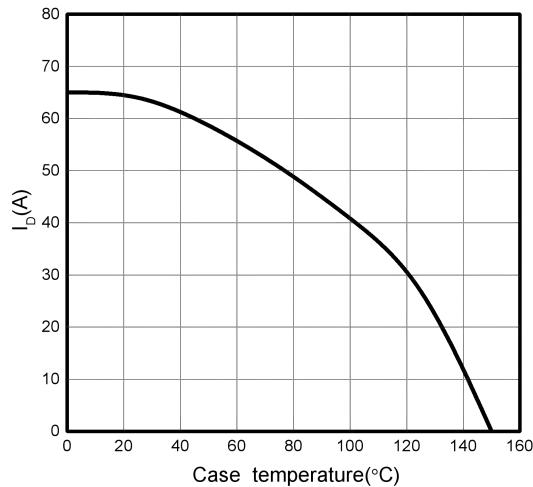


Figure 12.Power Dissipation vs.Temperature

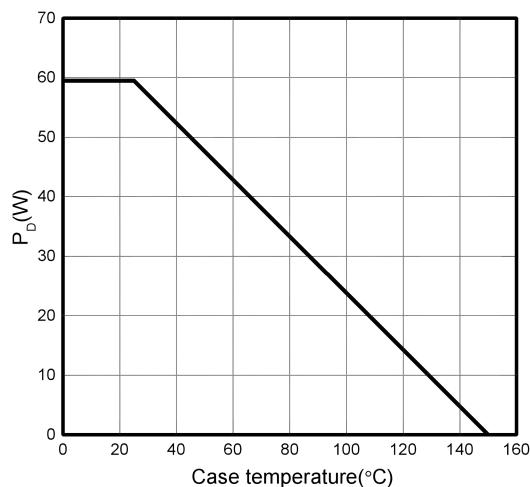
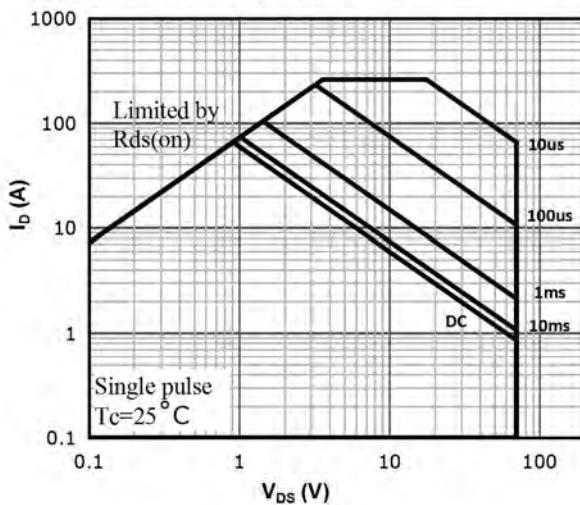
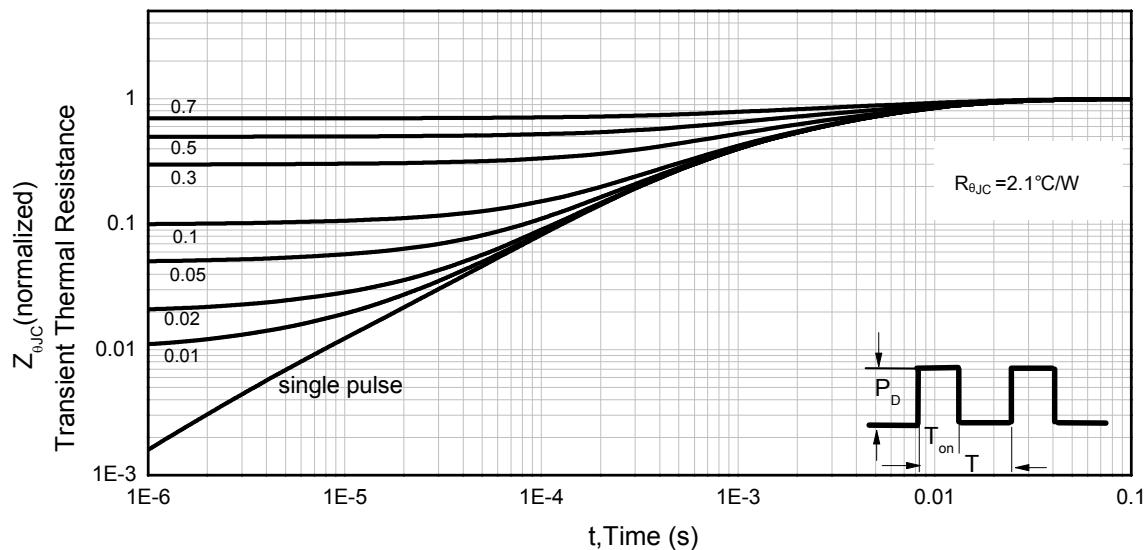
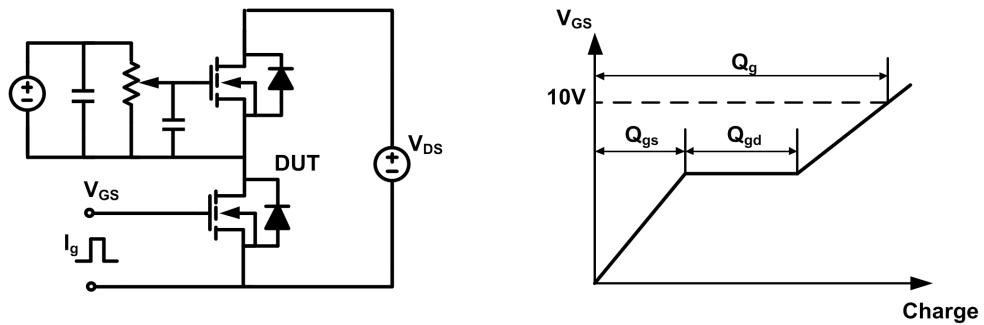


Figure 13: Safe Operating Area

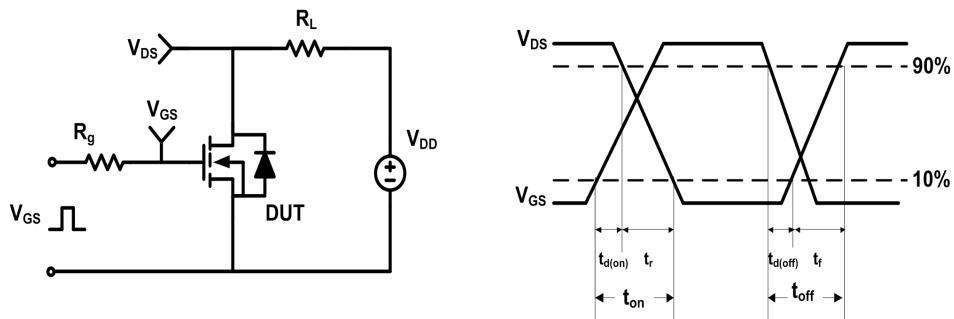
Figure 14. Normalized Maximum Transient Thermal Impedance ($R_{θJC}$)

Test Circuit & Waveforms

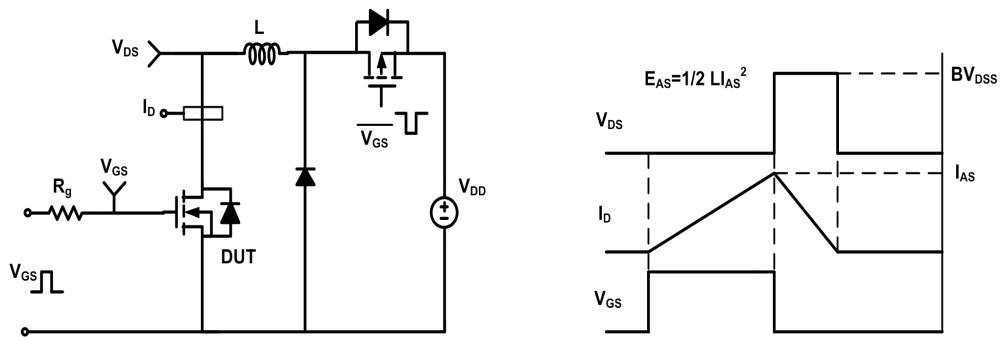
Gate Charge Test Circuit & Waveform



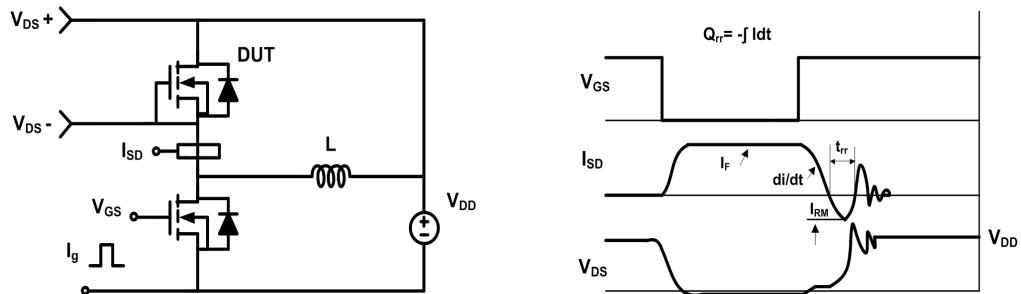
Resistive Switching Test Circuit & Waveform

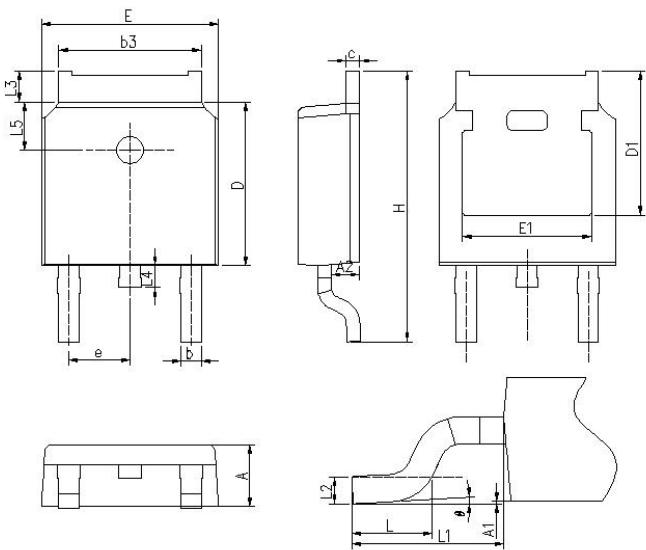


Unclamped Inductive Switching (UIS) Test Circuit & Waveform



Diode Recovery Test Circuit & Waveform



Mechanical Dimensions for TO-252

DIMENSIONS IN MILLIMETERS		
SYMBOL	MIN	MAX
A	2.18	2.4
A1	-	0.2
A2	0.9	1.17
b	0.65	0.9
b3	4.95	5.5
c	0.43	0.89
D	5.97	6.22
D1	5.21	-
E	6.35	6.8
E1	4.32	-
e	2.286BSC	
H	9.4	10.5
L	0.38	1.78
L1	2.90BSC	
L2	0.51BSC	
L3	0.88	1.28
L4	-	1.02
L5	1.65	1.95
θ	0°	10°